

Electronic Acknowledgement Receipt

EFS ID:	3570078
Application Number:	10730619
International Application Number:	
Confirmation Number:	4277
Title of Invention:	Method and circuit configuration for transmitting data between a processor and a hardware arithmetic-logic unit
First Named Inventor/Applicant Name:	Burkhard Becker
Customer Number:	27346
Filer:	Laurence Arnold Greenberg
Filer Authorized By:	
Attorney Docket Number:	L&L-I0225
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Authorized User	GREENBERG,LAURENCE A

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Notice of Appeal Filed	LL-I0225-Appeal.pdf	46008	no	2
			6560bae68816adcd42cafb2522889b12e00fa997		

Warnings:**Information:**

2	Fee Worksheet (PTO-06)	fee-info.pdf	8188	no	2
			e17c777076c64df77ac3d9ee98993ffa11db4110		

Warnings:**Information:**

Total Files Size (in bytes):			54196
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

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